

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination EICKEMEYER ET AL.	
		Examiner Amol V. Gole	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-6,629,271 B1	09-2003	Lee et al.	714/49
B	US-2002/0078317 A1	06-2002	Yasoshima, Hiroyuki	711/171
C	US-6,311,261 B1	10-2001	Chamdani et al.	712/23
D	US-5,623,608 A	04-1997	Ng, Spencer W.-F.	711/137
E	US-2001/0032307 A1	10-2001	ROHLMAN et al.	712/219
F	US-6,507,921 B1	01-2003	Buser et al.	714/45
G	US-6,075,931 A	06-2000	Panwar, Ramesh	716/1
H	US-2003/0105944 A1	06-2003	Emer et al.	712/220
I	US-5,465,120 A	11-1995	Schultheiss, John C.	348/716
J	US-2003/0061258 A1	03-2003	Rodgers et al.	709/102
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Gulati, M., Bagherzadeh, N. "Performance Study of a Multithreaded Superscalar Microprocessor". Proc. of the 2 nd International Symposium on High-Performance Computer Architectures, February 1996, 291-301
V	Ponomarev, D., Kucuk, G., Ghose, K., "Dynamic Allocation of Datapath Resources for Low Power", in Proc. of Workshop on Complexity-Effective Design, held in conjunction with ISCA-28, June 2001
W	Daniele Folegnani , Antonio González, "Energy-effective issue logic", Proceedings of the 28th annual international symposium on Computer architecture, p.230-239, June 30-July 04, 2001, Göteborg, Sweden
X	S. Reinhardt and S. Mukherjee, "Transient fault detection via simultaneous multithreading" 27th Int'l Symp. on Computer Architecture, June 2000

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.